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2145679977

Examiner: Nguyen, Kevin M.

P.02

SEP 1 5 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE Art Unit: 2674

Applicant: Morgan et al.

Serial No.: 09/088,674

Filed: June 2, 1998

Docket No. TI-25995 Boundary Dispersion for Mitigating PWM Temporal Contouring Artifacts in Digital

NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

15 September 2004

Assistant Commissioner for Patents Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. >1.8(A) I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner For Patents, PO Box 1450, Alexandria, Virginia 22313-1450 on the date shown below.

Dear Sir:

Applicants hereby appeal to the Board of Appeals from the decision dated 14 June 2004 of the Primary Examiner finally rejecting Claims 1-10.

The applicant believes no fee is due as the Notice Of Appeal fee was previously submitted in this application on 1 April 2002. Nevertheless, should any fee be due, please charge the fee of \$320.00 to the deposit account of Texas Instruments Incorporated, Deposit Account No. 20-0668.

The Commissioner of Patents is hereby authorized to charge any fees that may be required, including extension of time fees, or credit any overpayment to the deposit account of Texas Instruments Incorporated, Deposit Account No. 20-0668.

This form is submitted in triplicate.

Respectfully submitted,

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Charles A. Brill Reg. No. 37,786

Texas Instruments Incorporated PO Box 655474 M/S 3999 Dallas, TX 75265 (972) 917-4379

FAX: (972) 917-4418

Examiner: Nguyen, Kevin M.

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PAGE 3/4 * RCVD AT 9/15/2004 1:55:52 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/6 * DNIS:8729306 * CSID:2145679977 * DURATION (mm-ss):02-00

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Morgan et al.

Art Unit: 2674

Serial No.: 09/088,674

Examiner: Nguyen, Kevin M.

Filed: 06/02/1998

Docket No. TI-25995

For: B

BOUNDARY DISPERSION FOR MITIGATING PWM TEMPORAL CONTOURING

ARTIFACTS IN DIGITAL DISPLAYS

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below:

Charles A. Brill

September 15, 2004

Date

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NAME OF INVENTOR(S):		RECEIPT	DATE & SERIAL NO.:
Morgan et al.		Application No.: 09/088,674	
		Filing Date: 2 June 1998	
TITLE OF INVENTION: Boundary Dispersion for Mitigating PWM		Lines	Mara. Come 1999
Temporal Contouring Artifacts in Digital Displays			
TI FILE NO.:	DEFOSIT ROOT. 100.		
TI-25995	20-0668		
FAXED: 09/15/2004 DUE: 9/14/2004			_
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